



10, 456, 425

PATENT

CSC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Michael H. Perrott, et al.

Assignee: Silicon Laboratories Inc.

Title: DIGITALLY-SYNTHESIZED LOOP FILTER CIRCUIT
PARTICULARLY USEFUL FOR A PHASE LOCKED LOOP

Patent No.: 6,765,445 B2

Issued: July 20, 2004

Atty. Docket No.: 026-0002-1

Certificate
AUG 30 2004
of Correction

August 20, 2004

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT—
PTO MISTAKE (37 C.F.R. § 1.322(a))**

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

1. Attached hereto, in duplicate, is Form PTO-1050, with at least one copy suitable for printing.
2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:
Application filed September 5, 2003, page 29, line 2.
3. Please send the Certificate to:

Andrew C. Graham
ZAGORIN, O'BRIEN & GRAHAM, L.L.P.
7600B N. CAPITAL OF TEXAS HWY, SUITE 350
AUSTIN, TEXAS 78731

Respectfully submitted,

Andrew C. Graham, Reg. No. 36,531
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AUG 31 2004



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Attorney for Applicant(s)
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(Also Form PTO-1050)

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PATENT NO : 6,765,445 *B2*

DATED : July 20, 2004

INVENTOR(S) : Michael H. Perrott, Rex T. Baird, and Yunteng Huang

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 19, line 2: the words "a digital phase" should read -- a digital phase detector --.

MAILING ADDRESS OF SENDER:

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